| Reg.No.: |
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VIVEKANANDHA COLLEGE OF ENGINEERING FOR WOMEN

[AUTONOMOUS INSTITUTION AFFILIATED TO ANNA UNIVERSITY, CHENNAI] Elayampalayam – 637 205, Tiruchengode, Namakkal Dt., Tamil Nadu.

Question Paper Code: 7015

B.E. / B.Tech. DEGREE END-SEMESTER EXAMINATIONS - MAY / JUNE 2024

Fifth Semester

Computer Science and Technology

U19EC528 – EMBEDDED SYSTEMS DESIGN

(Regulation 2019)

Time: Three Hours

Maximum: 100 Marks

Answer ALL the questions

| Knowledge Levels | K1 – Remembering | K3 – Applying | K5 - Evaluating | | |
|------------------|--------------------|----------------|-----------------|--|--|
| (KL) | K2 – Understanding | K4 – Analyzing | K6 - Creating | | |

PART - A

| | | $(10 \times 2 =$ | 20 M | arks) |
|-------|--|------------------|------|-------|
| Q.No. | Questions | Marks | KL | CO |
| 1. | Distinguish between the Von Neumann and Harvard microcontrollers. | 2 | K2 | CO1 |
| 2. | Classify embedded system based on the applications. | 2 | K2 | CO1 |
| 3. | Write an ALP for ARM processor to find the factorial of a given number. | 2 | K1 | CO2 |
| 4. | List the instruction sets of LPC2148 microcontroller. | 2 | K1 | CO2 |
| 5. | For a GPIO pin to be made to act as an ON/OFF switch, which are the registers to be used in LPC2148? | 2 | K1 | CO3 |
| 6. | Write short notes on Interrupts. | 2 | K2 | CO3 |
| 7. | Write an ALP to rotate the stepper motor clockwise / anticlockwise continuously with full step sequence. | 2 | K1 | CO4 |
| 8. | Sketch the connection diagram of DAC with microcontroller | 2 | K1 | CO4 |
| 9. | Justify the advantages of mail boxes in RTOS. | 2 | K1 | CO5 |
| 10. | Define Mutex. | 2 | K2 | CO5 |

PART – B

| | | | (| $(5 \times 13 =$ | 65 M | arks) |
|-------|-----|--|------|------------------|--------|--------|
| Q.N | lo. | Questions | | Marks | KL | CO |
| 11. | a) | Outline the components involved in an embedded systematchitecture. (OR) | em | 13 | K2 | CO1 |
| | b) | | | 13 | K2 | CO1 |
| 12. | a) | LPC2148 microcontroller. | l in | 13 | K2 | CO2 |
| | b) | (OR) Draw the architecture of ARM processor and explain functional units. | its | 13 | K2 | CO2 |
| 13. | a) | microcontroller. | 148 | 13 | K2 | CO3 |
| | b) | (OR) Discuss about the interfacing and operation of ADC and DAC LPC2148 microcontroller. | in | 13 | K2 | CO3 |
| 14. | a) | Elaborate the steps to be followed to interface LCD with processor. (OR) | ı a | 13 | K2 | CO4 |
| | b) | | h a | 13 | K2 | CO4 |
| 15. | a) | and handling of interrupt source calls. | ent | 13 | K2 | CO5 |
| | b) | (OR) Explain in detail about multiple processes and threads with application. | an | 13 | K2 | CO5 |
| | | PART – C | | | | |
| | | | | (1 x 15 | = 15 N | Marks) |
| Q.No. | | Questions | ľ | Marks | KL | CO |
| 16. a | | With suitable diagrams and sample codes, illustrate how a steppe motor can be interfaced with processor. | er | 15 | K3 | CO4 |
| | | (OR) | | 1.5 | 77.0 | 005 |
| b |) . | Develop a system for any real time application using RTOS. | | 15 | K3 | CO5 |